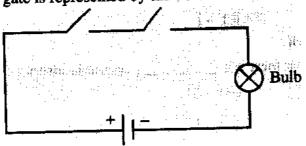
COMPUTER SCIENCE & ENGINEERING PART - I

Ea	ch question carries one mark.		F0 . 4 . 40
		ere are	$50 \times 1 = 50$ possible distinct operators.
		\$ 18 m	
(C)	n/2		_
In v	which addressing mode the operan	d is giv	en explicitly in the instruction of
(A)	Absolute	•	Immediate
(C)	Indirect		Direct
For	a pipelined CPU with a single AL	U, con	sider the following situations.
1.	The J+1th instruction uses the re	sult of	the Ith instruction as an operand
2.	The execution of a conditional i	ump ins	Struction
3.	The Jth and J + 1th instructions re	cuire f	he AI II at the same time
Whi	ich of the above can cause a hazare	1 2	: Also at the same time
			2 & 3 only
(C)	3 only	(D)	All the three
Whi	ch of the following is incorrect?		
1.		the CDI	T write for the TIO 1
2.	In the Interrupt driven I/O device	inform	os the CDI of its and t
3. .	In DMA, the CPU sends its I/O transaction.	to the	DMA controller which manages the entire
(A)	1 and 2	(B)	2 and 3
(C)	1, 2 and 3	(D)	None of the above
001	1010110100100, the exce	ss-3 co	le shown is equivalent to decimal
(A)	2391	(B) ·	0271
(C)	5642	(D)	0358
	In to (A) (C) In (A) (C) For 1. 2. 3. Wh. (A) (C) Whit 1. 2. 3. (A) (C) (C) (A)	(A) 2n (C) n/2 In which addressing mode the operant (A) Absolute (C) Indirect For a pipelined CPU with a single AL 1. The J+1 th instruction uses the re 2. The execution of a conditional july 3. The J th and J + 1 th instructions re Which of the above can cause a hazard (A) 1 & 2 only (C) 3 only Which of the following is incorrect? 1. In the programmed I/O method, to 2. In the Interrupt driven I/O device 3. In DMA, the CPU sends its I/O transaction. (A) 1 and 2 (C) 1, 2 and 3 0 0 1 1 0 1 0 1 1 0 1 0 0 1 0 0, the excess (A) 2391	n bits in operation code imply that there are (A) 2n (B) (C) n/2 (D) In which addressing mode the operand is giv (A) Absolute (B) (C) Indirect (D) For a pipelined CPU with a single ALU, conducted in the conduction of a conditional jump in the conduction of a conditional jump in the conduction of the above can cause a hazard? (A) 1 & 2 only (B) (C) 3 only (D) Which of the following is incorrect? In the programmed I/O method, the CPU conduction in the conduction in th

6. What logic gate is represented by the circuit shown below?



(A) AND

(B) NAND

(C) NOR

- (D) EQUIVALENCE
- 7. The minterms corresponding to decimal number 15 is
 - (A) ABCD

(B) ABCD

(C) A' + B' + C' + D'

- $(D) \quad A+B+C+D$
- 8. How many different trees are possible with 10 nodes?
 - (A) 1014

(B) 1200

(C) 50

- (D) 68
- 9. In an AVL tree, at what condition the balancing is to be done?
 - (A) Pivotal value is equal to zero
 - (B) Pivotal value not equal to zero
 - (C) If the pivotal is greater than 1 or less than 1
 - (D) If the pivotal value is infinity
- 10. A 3-ary tree in which every internal node has exactly 3 children. The number of leaf nodes in such a tree with 6 internal nodes will be
 - (A) 10

(B) 11

(C) 12

- (D) 13
- 11. The number of nodes in the largest maximal independent set of the complete bipartite graph K(4, 2) is
 - (A) 2

(B) 3

(C) 4

(D) 6

Space For Rough Work

:1:

(A)	Log 2n nodes	(B)	n + 1 nodes
(C)	2n nodes	(D)	2n + 1 nodes
Algo	orithm which solves the all pairs sh	ortest j	path problem is
(A)	Dijkstra's algorithm	(B)	Floyd's algorithm
(C)	Prim's algorithm	(D)	Warshall's algorithm
		tree of	height h is
(A)	2 ^h	(B)	2h-1 -1 11 11 11 11 11 11 11 11 11 11 11 1
(C)	2 ^{h+1} -1	(D)	2 ^{h+1}
•			14.12
The	algorithm design technique used in	the qu	nick sort algorithm is
(A)	Dynamic programming	(B)	Backtracking
(C)	Divide and Conquer	(D)	Greedy method
How	many distinct binary search trees	can be	created out of 4 distinct keys?
	•	(B)	24
(C)	14	(D)	5
10,	1, 3, 5, 15, 12, and 16. What is the	e heigl	nt of the binary search tree (the height is the
(A)	4	(B)	6
(C)	2	(D)	3
Wha	at is the purpose of flow control?		
	• •	ed if an	a acknowledgment is not received.
• •			٠,
	•		
		_	- A
	Spaces	or Ro	igh: Work
	Algo (A) (C) The max (A) (C) The (A) (C) The 10, max (A) (C)	Algorithm which solves the all pairs she (A) Dijkstra's algorithm (C) Prim's algorithm The height of a binary tree is the maximaximum number of nodes in a binary (A) 2h (C) 2h+1-1 The algorithm design technique used in (A) Dynamic programming (C) Divide and Conquer How many distinct binary search trees (A) 42 (C) 14 The following numbers are inserted in 10, 1, 3, 5, 15, 12, and 16. What is the maximum distance of a leaf node from (A) 4 (C) 2 What is the purpose of flow control? (A) To ensure that data is retransmitted. (B) To reassemble segments in the control? (C) To provide a means for the received. (D) To regulate the size of each segments.	Algorithm which solves the all pairs shortest process. (A) Dijkstra's algorithm (B) (C) Prim's algorithm (D) The height of a binary tree is the maximum maximum number of nodes in a binary tree of the control of th

	(A)	Layer 2	(B)	Layer 3	Let the State	
	(C)	Layer 4	(D)	Layer 7	e s ≹ke-	
20.		t is the powerful, centralized essing tasks on behalf of clients			vices?	_
	(A)	Client	(B)	Host comp	outer	
. ‡ *	(C)	Terminal	(D)	Network	a de la companya de l	
	T'					
21.		r control is needed at the transp	ort layer t	ecause of po	otential errors occui	ring
	(A)	from transmission line noise.				
	(B)	in routers.				
	(C)	from out-of-sequence delivery	•			•
	(D)	from packet losses.			The Section of Contract Contra	
					1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
22.	Whie I	ch of the following is responsib	ole for app	proving stan	dards and allocating	g resources in
	(A)	Internet Architecture Board (I	AB)			. A.
	(B)	Internet Engineering Task For	ce (IETF)			. \$
	(C)	Inter NIC				
		7. T. C. 1		1	Market State of State	
23.	Whi	ch of the following is true when	describin	ıg a multicas	st address?	
	(A)	Packets addressed to a unicast	address a	re delivered	to a single interface	e.
	(B)	Packets are delivered to all in one-to-many address.	terfaces id	lentified by	the address. This is	also called a
	(C)	Identifies multiple interfaces a also be called one-to-one-of-m		y delivered	to one address. Thi	s address can
:	(D)	These addresses are meant for unique so it is unlikely they w				most globally
		Spac	e For Rot	ıgh: Work	<u> </u>	·

19. Acknowledgements, sequencing, and flow control are characteristics of which OSI layer?

- Which of the following is TRUE for the language { a^P : P is Prime} ?
 (A) It is not accepted by Turing Machine.
 (B) It is regular but not context free.
 (C) It is context free but not regular.
 (D) It is neither regular nor context free but accepted by Turing Machine.
- 25. Fortran is
 - (A) Regular language
 - (B) Context free language
 - (C) Context sensitive language
 - (D) None of the above
- 26. The equivalent regular expression for the regular expression (aa+bb+ab+ba)*
 - (A) ((a+b),(a+b))*

(B) (aba+bab+bb)*

(C) ((a+b)+(a+b))*

- (D) None of the above
- 27. Turing machine is capable of accepting
 - (A) Only Regular Languages and Context Free Languages
 - (B) Only Context Sensitive Languages
 - (C) Recursively Enumerable Languages only
 - (D) All four categories of Languages
- 28. Context grammar is ambiguous if
 - (A) the grammar contains useless non-terminals
 - (B) it produces more than one parse tree for same sentence
 - (C) some production has two non-terminals side by side on right hand side
 - (D) None of the above
- 29. In the design of lexical analyzer
 - (A) Only finite automata is used
 - (B) Only regular expression is used
 - (C) Both finite automata and regular expression are used
 - (D) Both finite automata and regular expression not are used

30.	The :	string 1101 does not below	ig to the set represented by Table 1839	i,
	(A)	110*(0+.1)	(B) 1(0+1)*101 (4 b 2) (3 c)	
	(C)	(10)*(01)*(00+11)*	(D) (00+(11)*0)*	
			३७ मार १४ % । १	•
31.	The	macro processor must per	form _{et es} apolitic filmster en an #6000000000000000000000000000000000000	
	(A)	recognize macro definit	ions and macro calls	
	(B)	save the macro definitio	18	•
	(C)	expand Macro calls		•
	(D)	all of these	Secretary of the second	
			and the state of t	
32.	Relo	cation bits used by reloca	ting loader are specified by	
		relocating loader itself	(B) linker	
	(C)	assembler - Section 1997	microprocessor programme and an	
			of the second second	
33.		mputer cannot 'boot' if it		
	• •	Compiler	(B) Loader	
	(C)	Operating System	(D) Assembler & David Communication	
			review Fig The estimate will be	
34.		er is	Appropriate to the Section of the Control of the Co	
	• /	same as loader	Supersymmetry and a state of the supersymmetry of t	
		•	module the agree of the first the control of	
		user source code as inpu		
	(D)	always used before prog	rams are executed with the first of supplier of the	
	TD1	3 Cd 15	Springer of which is the long to the second	
35.			a Set of order n is	
	(A)		(B), 2n	
	(C)	n²	(D) 2^n $\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$	
36.	Whi	ch of the following stater	nents is FALSE?	,
50.	(A)	-	pers is an Abelian group under addition.	
	(B)		ers is an Abelian group under addition.	
	(C)		pers form an Abelian group under multiplication.	
	(C) (D)	None of the above.	roses and the second se	
	(1)	1 tollo of the gooter	the state of signal of the control of the state of the st	

37.	If every element of a group G is its own inverse, then G is								
		Finite	(B)	Infinite	e [*] z				
	(C)	Cyclic	(D)	Abelian					
38.	A sel	f-complemented distributive lattice	e is ca	lled					
	(A)	Boolean Algebra	(B)	Modular Lattice		•			
	(C)	Complete Lattice	(D)	Self Dual Lattice					
39.	Hass	e diagrams are drawn for							
		Partially ordered sets	(B)	Lattices					
		Equivalence relations	(D)	None of the above					
40.	The	number of different permutations of	of the	word BANANA is					
	(A)	720	(B)	60					
	(C)	120	(D)	360					
41.	Whie	ch of the following is not a fundam	nental	process state?					
	(A)	Ready	(B)	Terminated					
	(C)	Executing	(D)	Blocked					
42.	Poor	response time is usually caused by	y						
	(A)	Process busy	(B)	High I/O rates					
	(C)	High paging rates	(D)	Any of the above	•				
43.	SST	F stands for			i.				
	(A)	Shortest-Seek-Time-First schedu	ıling	·					
	(B)	Small-Small-Time-First	•						
	(C)	Simple-Seek-Time-First schedul	ing	•					
	(D)	Small-Simple-Time-First							
44.	Virt	hial memory is							
	(A)								
	(B)			g systems					
	(C)	less efficient in utilization of me	mory						
	(D) useful when fast I/O devices are not available								

45.		avoid race condition, de the critical section	the maximum nun	mber of processes that may be simultaneou	ısly
	(A)		(B)) one	
	(C)	two	(D)	•	
46.	CPU	is a techni J allocation.	que of improving	the priority of process waiting in Queue	for
	(A)	Starvation	(B)	Ageing	
	(C)	Revocation	(D)	Relocation	
47.	In th	ne Normal fo	rm, a composite att	tribute is converted to individual attributes.	
	(A)	First	(B)		
	(C)	Third	(D)	Fourth	
48.	G1111C	material labies called.	eased on the conce	ept that data is organized and stored in tw	7 0-
	(A)	Fields	(B)	Records	
	(C)	Relations	(D)	Keys	
49.		specifies a sear	rch condition for a g	group or an aggregate.	
	(A)	GROUP BY Clause		HAVING Clause	•
	(C)	FROM Clause	(D)	WHERE Clause	
50.	How	DOM differs from SA	AX ?		
	(A)	DOM is not event dri	ven and builds up t	the whole memory.	
	(B)	SAX is event driven			
	(C)	DOM and SAX are p	. -		
	(D)	None of these.	÷.		
			Space For Roug	igh Work	

Each question carries two marks.

 $25 \times 2 = 50$

- 51. If memory access takes 20 ns with cache and 110 ns without it, then the hit ratio (cache uses a 10 ns memory) is
 - (A) 93%

(B) 90%

(C) 87%

- (D) 88%
- 52. A hard disk with a transfer rate of 10 M bytes/second is constantly transferring data to memory using DMA. The Processor runs at 600 MHz. and takes 300 and 900 clock cycles to initiate and complete DMA transfer respectively. If the size of the transfer is 20 K bytes, what is the percentage of processor time consumed for the transfer operation?
 - (A) 0.1 %

(B) 5.0 %

(C) 1.0 %

- (D) 0.5 %
- 53. In serial communication employing 8 data bits, a parity bit and 2 stop bits, the minimum band rate required to sustain a transfer rate of 3000 characters per second is
 - (A) 2400 baud

(B) 19200 baud

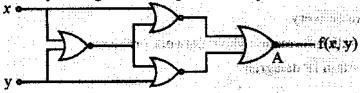
(C) 4800 baud

- (D) 1200 baud
- 54. What is the content of Stack Pointer (SP)?
 - (A) Address of the current instruction
 - (B) Address of the next instruction
 - (C) Address of the top element of the stack
 - (D) Size of the stack
- 55. $[(A+A'B)(A+A'B')][(CD+C'D')+(C\oplus D)] =$
 - (A) B

(B) A

 (\mathbf{C}) 0

- (D) 1
- 56. Identify the logic function performed by the circuit shown



(A) Exclusive OR

(B) Exclusive NOR

(C) NAND

(D) NOR

57.	elen and	nents. Assume that the is	nsertion and dele Ex variables, re	etion operations are spectively. Initially	nented with an array of ne carried out using REAR, REAR=FRONT=0.The
	(A)		-	ipiy are	
	(4.4)	empty:REAR==FRON			21.4
	(B)				$\phi_{1}(\mathbf{I})$
	(-)	empty:(FRONT+1)MC			
	(C)	full:REAR==FRONT			7 (A)
		empty:(REAR+1)mod	a==FRONT		
	(D)	+ - · · · · · · · · · · · · · · · · · ·	==REAR		
58.	The prob	recurrent relation captu blem with n discs is	iring the optima	al execution time	of the Towers of Hanoi
	(A)	T(n)=2T(n-2)+2	(B)	T(n)=2T(n-1)+n	• • • • • • • • • • • • • • • • • • •
	(C)	T(n)=2T(n/2)+1	(D)	T(n)=2T(n-1)+1	
59.	depe	ending techniques takes to catenated, and the process	he value of its le	east significant byte	element on a smaller list, e. Then the small lists are cant byte until the list is
	(A)	Radix sort	(B)	Quick sort	
	(C)	Heap sort	(D)	Merge sort	
60.	Whi	ch statements are true reg	earding ICMP pa	ckets?	
	1.	They acknowledge rece			
	2	They guarantee detaors	-		

- 2. They guarantee datagram delivery.
- 3. They can provide hosts with information about network problems.
- 4. They are encapsulated within IP datagram.
- (A) 1 only

(B) 2 and 3

(C) 3 and 4

(D) 2, 3 and 4

61.	Why does the	data communication industr	y use the la	avered OSI reference m	odel '	?
-----	--------------	----------------------------	--------------	------------------------	--------	---

- 1. It divides the network communication process into smaller and simpler components, thus aiding component development, design, and troubleshooting.
- 2. It enables equipment from different vendors to use the same electronic components, thus saving research and development funds.
- 3. It supports the evolution of multiple competing standards and thus provides business opportunities for equipment manufacturers.
- 4. It encourages industry standardization by defining what functions occur at each layer of the model.
- (A) 1 only

(B) 1 and 4

(C) 2 and 3

(D) 3 only

62. What are two purposes for segmentation with a bridge?

- 1. To add more broadcast domains.
- 2. To create more collision domains.
- 3. To add more bandwidth for users.
- 4. To allow more broadcasts for users.
- (A) 1 only

(B) 2 and 3

(C) 2 and 4

(D) 4 only

63. Consider the languages:

 $L1 = \{a^nb^nc^m: n,m > 0\}$ and $L2 = \{a^nb^mc^n: n,m > 0\}$. Which of the following statements is FALSE?

- (A) L1∩L2 is a context free language
- (B) L1.L2 is a context free language
- (C) L1 and L2 are context free languages
- (D) L1∩L2 is a context sensitive language

64. Which one of the following is not decidable?

- (A) Given a Turing Machine M, a string s and an integer k, M accepts s within k steps.
- (B) Equivalence of two Turing Machines.
- (C) Languages accepted by given finite state machine is non-empty.
- (D) Languages accepted by a context free grammar is non-empty.

1:

65.	A bottom-up parser generates			
	(A) LMD	(B)	, RMD	
	(C) RMD in reverse	₂ (D)	LMD in reverse	
			$(\mathfrak{A}^{(n)})^{(n)} = (-1)^{\mathfrak{A}} (\mathfrak{A}^{(n)})^{(n)} = (-1)^{\mathfrak{A}}$	•. · · · · · · · · · · · · · · · · · · ·
66.	Backtracking is a problem associate			
	(A) Topdown Parsing			
	(C) Both (A) and (B)	, ,	None of the above	And the second
47			1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1	
67.	The function f: $Z \rightarrow z$ is given by f(-		THE WALL
	(A) One-to-one	(B)	On to	†
	(C) One to One and onto	,, ,), (D)	None of the above	* .
68.	A U B = A \cap B if and only if			
	(A) A is empty set	(B)	B is empty set	1 ex
	(C) A and B are non-empty sets	(D)	A and B are empty sets	$\Omega^{2,0}$
	(+)	(-)		,• ·
69.	Every finite subset of lattice has		•	
	(A) a LUB and a GLB	(B)	Many LUBs and a GLE	
	(C) Many LUBs and many GLBs	, (D)	Either some LUBs and	some GLBs
=0	O 11 TO THE OIL 11 11 11	in .		5 14 (DED) 51
70.	Consider an XML file called in intro.dtd as follows:	tro.xmi ar	nd a document type de	minition (DID) file
	Intro.xml			4. 4.
	< ?xml version = "1.0" ?>			
	myMessage SYSTE</td <td>M "intro di</td> <td>M"\</td> <td></td>	M "intro di	M"\	
	<pre><mymessage></mymessage></pre>	VI 111110.0	47	. · · · · · · · · · · · · · · · · · · ·
	<pre><message>Welcome to XML</message></pre>	scone>		
	<pre></pre>	ssage-	and the second s	
	Intro.dtd			
	ELEMENT myMessage(message</td <td>.)~</td> <td></td> <td></td>	.)~		
	ELEMENT message(#PCDATA</td <td></td> <td></td> <td>4 W 1 W 2</td>			4 W 1 W 2
	A validating parser will classify int	io.xini as		e de la companya de l
	(A) Well-formed and validated	.1	And the second of	a 1
	(B) Well-formed but not validate			
	(C) Validated but not well-forme			
	 (D) Neither validated not well-for 	rmed		

71.	Let state	S and Q be two s ements wait(S);	emaphores init	ialized	to 1, where	re P0 and P1 processes the following
			P0		P1	
			wait(Q);		wait(Q);	
			;		wait(S);	
			signal(S);	0		
			signal(Q);		signal(Q);	•
					signal(S);	
	resp	ectively. The above	e situation der	oicts a _		•
	(A)		-	(B)		
	(C)	Signal		(D)	Interrup	pt
72.	FCF (A)	'S if the disk queu	cated initially e of I/O blocks	at 32, reques (B) (D)	ts are 98, 3 324	number of disk moves required with 37,14,124,65,67.
73.	Con	sider a logical ad	dress space of	·8 page	s of 1024	words mapped into memory of 32
	man	ies. How man y bit	s are there in the	he phys	ical addres	ss?
	, ,	9 bits		(B)	11 bits	e de la companya de l
	(C)	13 bits		(D)	15 bits	
74.	Usin the f	g the SQL GROU	JP BY phrases?	with a	SELECT :	statement can help detect which of
	(A)	The multivalue,	multicolumn pr	roblem		
	(B) (C)	The inconsistent The missing value	values problen			
	(D)	The general-purp		olumn p	roblem	
				•	انه. در	
75.	10800	access the same 1	ine system firs	st acces	ses first 10	t has 4 page frames with no pages 00 distinct pages in some order and e order. How many page faults will
	(A)	196		(B)	192	
	(C)	197		(D)	195	
			Space	For Ro	ugh Work	