R09

Code No: D5501

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.TECH II - SEMESTER EXAMINATIONS, APRIL/MAY 2012
HARDWARE SOFTWARE CO-DESIGN
(EMBEDDED SYSTEMS)

Time: 3hours **Max.Marks:60**

Answer any five questions
All questions carry equal marks
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1. Explain the following partitioning algorithms.
   a) Cosyma
   b) Vulcan.

2. What is emulation? Briefly discuss the various prototyping and emulation techniques.

3. (a)Explain in brief the various steps in co design.
   (b)Explain the concept of Distributed system co synthesis.

4. What is meant by compilation? Discuss about practical considerations in a compiler development environment.

5. Define the following terms and explain them in brief.
   (a) Design and Co-design
   (b) Interface Verification.

6. Describe various system level specification languages and compare any two of them.

7. Explain the three principle compiler tasks. Discuss the problems of traditional compilation.

8. Explain in detail the approaches for hardware validation and differentiate them.

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