R09

Code No: D5501

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech II - Semester Examinations, March/April 2011
HARDWARE SOFT WARE CO-DESIGN
(EMBEDDED SYSTEMS)

Time: 3hours **Max.Marks:60**

Answer any five questions
All questions carry equal marks
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1. a) What is meant by co-synthesis? Describe the distributed system Co-Synthesis.
   b) Discuss about co-design languages. [12]

2. a) Discuss the future developments in emulation and prototyping.
   b) Discuss about the system communication infra structure. [12]

3. a) Write the features of various application system classes.
   b) Discuss about the needs of embedded software development. [12]

4. a) Describe the problems occurred when adapting traditional compilation model to embedded processor.
   b) Explain about Source debugging. [12]

5. a) Write notes on synchronous and asynchronous computations.
   b) What is meant by Co-ordinating concurrent computations? Explain. [12]

6. a) Explain the over view of Lycos system.
   b) Distinguish between design specialization and verification. [12]

7. a) List out the features of multi language co-simulation.
   b) Explain about FSMD representation. [12]

8. Write any **two** of the following.
   i) Prototyping techniques.
   ii) Euclid's gcd algorithm.
   iii) Hilevel synthesis [12]

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