



# VIGNAN UNIVERSITY :: VADLAMUDI

## Time Table for B.Tech II Year II Semester Supplementary Examinations - December 2011

(2009 admitted batch)

Examination Time: 2.00 PM to 5.00 PM

Date	BT	CHEM	CIVIL	CSE	ECE	EEE	IT	MECH	MECHTRONICS
<b>19.12.2011 (Mon)</b>	Instrumental Methods of Analysis BT206	Process Heat Transfer CH206	Structural Analysis- I CE202	Principles of Programming Languages CS210	Pulse and Digital Circuits EC206	Pulse and Digital Circuits EC206	Principles of Programming Languages CS210	Fluid Mechanics & Hydraulic Machines ME202	Thermal Engineering MT204
<b>20.12.2011 (Tues)</b>	Genetics BT204	Mechanical Unit Operations CH204	Building Construction and Planning CE204	Database Management Systems CS204	Electronic Circuit Analysis EC204	Power Systems – I EE204	Database Management Systems CS204	Production Processes ME204	Production Processes MT202
<b>21.12.2011 (Wed)</b>	Cell Biology BT202	Chemical Engineering Thermodynamics – I CH202	Hydraulics and Hydraulic Machines CE206	Design and Analysis of Algorithms CS206	Probability Theory and Stochastic Process EC202	A.C. Machines – I EE202	Design and Analysis of Algorithms CS206	Kinematics of Machines ME206	Digital Electronics MT206
<b>22.12.2011 (Thurs)</b>	Bioprocess Engineering BT208	Process Instrumentation CH208	Surveying – II CE208	Operating Systems CS208	Switching Theory and Logic Design EC208	Switching Theory and Logic Design EC208	Operating Systems CS208	Metrology & Instrumentation ME208	Theory of Machines MT208
<b>23.12.2011 (Fri)</b>	Thermodynamics for Biotechnologists CH214	Basic Electrical and Electronics Engineering EE101	Environmental Engineering – I CE210	Object Oriented Programming CS202	Analog Communications EC210	Control Systems EE206	Object Oriented Programming CS202	Thermal Engineering – I ME210	Process Instrumentation MT210
<b>24.12.2011 (Sat)</b>	Minor – II	Minor – II	Minor – II	Minor – II	Minor – II	Minor – II	Minor – II	Minor – II	Minor – II

**Minor – II Subjects:** Marketing and Human Resource Management (MS301)  
 General Studies – II (HS282)  
 Linear and Digital ICs (EC206)  
 Operating Systems (CS208)

**Director of Evaluation**

Copy to VC, Registrar, Director,DET, All HODs, TC, PC, Workshop I/C, Bus I/C, Reception, Web Administrator  
 VU/CIR/011/598 Dated: 27.09.2011 (TS)