# VIGNAN UNIVERSITY:: VADLAMUDI M.TECH I Year I Semester Supplementary Examinations December – 2011 (2009 & 2010 Admitted Batches)

## Examination Time: 10.00 AM to 01.00 PM

Date	Bio Technology & Bioprocess Engineering	Computer Science and Engg.	Embedded Systems	VLSI Design	
19.12.11 (Monday)		Advanced Database Management Systems (CS501)		ASIC Design (EC02P5904)	
21.12.11 (Wednesday)		Advanced Embedded System Concepts (EC505)			
23.12.11 (Friday)	Advanced Biochemical Reaction Engineering (BT01P1103)	Data Structures & Algorithms (CS505)	Digital & Analog IC Design (EC503)	Digital & Analog IC Design (EC02P5902)	
26.12.11 (Monday)		Advanced Computer Architecture (CS507) Applied Mathematics (EC551)		Basic VLSI Design (EC02P5901)	
28.12.11 (Wednesday)	Advanced Process Engineering Principles –I (BT01P1105)		Embedded Programming Languages (EC573)	Advanced Digital System Design (EC02P5903)	
30.12.11 (Friday)					

## **Controller of Examinations**

**Director of Evaluation** 

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## VIGNAN UNIVERSITY :: VADLAMUDI M.TECH I year II Semester Supplementary Examinations December – 2011 (2009 & 2010 Admitted Batches)

#### Examination Time: 10.00 AM to 1.00 PM

Date	Industrial Pollution Control Engg.	Computer Science and Engineering	Embedded Systems	Information Technology	Machine Design	VLSI Design
20.12.11 (Tuesday)	Environmental Legislation Audit and Impact Assessment (CH01P3203)	Object Oriented Analysis and Design (CS506)	RTOS for Embedded Systems (EC502)	Embedded Systems (CS01P4204)	Optimization Techniques (ME508)	DSP Processors and Architectures (EC01P5904)
22.12.11 (Thursday)	Instrumentation for Environmental Analysis (CH01P3204)		DSP Processors and Architectures (EC508)		Experimental Stress Analysis (ME516)	
24.12.11 (Saturday)			FPGA and CPLD Architectures and Applications (EC526)			
27.12.11 (Tuesday)			Embedded System Design (EC504)			
29.12.11 (Thursday)			Wireless Communications and Networks (EC506)			
31.12.11 (Saturday)			Micro Electro Mechanical Systems (EC536)			

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**Director of Evaluation**